



## COURSE DESCRIPTION CARD - SYLLABUS

Course name

PLC controllers and PLD programmable logic devices [N1Eltech2>PO12-SPLC]

### Course

Field of study

Electrical Engineering

Year/Semester

5/9

Area of study (specialization)

–

Profile of study

general academic

Level of study

first-cycle

Course offered in

Polish

Form of study

part-time

Requirements

elective

### Number of hours

Lecture

10

Laboratory classes

10

Other

0

Tutorials

0

Projects/seminars

10

### Number of credit points

4,00

### Coordinators

dr inż. Michał Krystkowiak

michal.krystkowiak@put.poznan.pl

### Lecturers

### Prerequisites

none

### Course objective

none

### Course-related learning outcomes

none

### Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

none

### Programme content

none

### Course topics

none

### Teaching methods

none

### Bibliography

none

### Breakdown of average student's workload

	Hours	ECTS
Total workload		
Classes requiring direct contact with the teacher		
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)		